

DESCRIPTION

BL8078Q is a group of positive voltage output, low power consumption, low dropout voltage regulator. It can provide output value of 3.3V or 5.0V and output current up to 150mA. It also provides foldback short-circuit protection, thermal protection and output current limit function. The maximum output current(150mA) must be used in the safe operation area. The very low power consumption of BL8078Q($I_q=2.5\mu A$) can greatly improve natural life of batteries.

BL8078Q includes high accuracy voltage reference, error amplifier, current limit circuit and output driver module.

BL8078Q has good load transient response and good temperature characteristics. And it uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$.

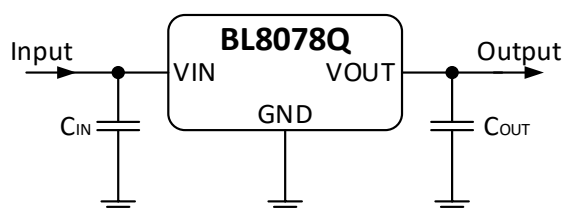
FEATURES

- Low power consumption: $2.5\mu A$ (Typ.)
- Maximum output current: 150mA (must be used in the safe operation area)
- Low dropout voltage:
240mV@50mA ($V_{OUT}=5.0V$)
335mV@50mA ($V_{OUT}=3.3V$)
- Wide input voltage range: 3V~40V
- Output voltage: 3.3V, 5.0V
- Highly accurate: $\pm 2\%$
- AEC-Q100 Grade2

APPLICATIONS

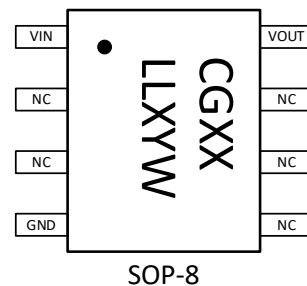
- Automotive
- Battery powered equipment
- Reference voltage source regulation after switching power
- Always-on battery applications
 - door modules
 - remote keyless-entry systems
 - immobilizers

TYPICAL APPLICATION



Note: Input capacitor ($C_{IN}=1\mu F$) and output capacitor ($C_{OUT}=1\mu F$) are recommended in all application circuit. Ceramic capacitor is recommended.

PIN OUT & MARKING



CG: Product code
XX: Output voltage
LL: Lot No.
X: Fab code
YW: Date code

BL8078Q

ORDERING INFORMATION

Part No.	Package	Tape&Reel
BL8078QCD8TR33	SOP-8	2500/reel
BL8078QCD8TR50	SOP-8	2500/reel

ABSOLUTE MAXIMUM RATING¹

Parameter		Value
Max input voltage		44V
Operating junction temperature (T _J)		150°C
Package thermal resistance (θ _{JC})	SOP-8	40°C/W
Package thermal resistance (θ _{JA})		80°C/W
Storage temperature (T _S)		-40°C to 150°C
Lead temperature & time		260°C, 10s
ESD (HBM ²)		>2000V

Note:

- 1) Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.
- 2) ESD susceptibility, HBM according to ANSI/ESDA/JEDEC JS001 (1.5KΩ, 100pF).

RECOMMENDED WORK CONDITIONS

Parameter	Min	Recommended	Max	Units
Input voltage range	3		40	V
Ambient temperature (T _A)	-40		105	°C

ELECTRICAL CHARACTERISTICS

Test conditions: C_{IN}=1μF, C_{OUT}=1μF, T_J=-40°C~+140°C, unless otherwise stated.

Typical values are given at T_A=25°C.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V _{IN}	Input Voltage		3		40	V
V _{OUT}	Output Voltage	V _{IN} -V _{OUT} =1V 1mA≤I _{OUT} ≤30mA	V _{OUT} *0.98	V _{OUT}	V _{OUT} *1.02	V
I _{OUT (Max.)}	Maximum Output Current	V _{IN} -V _{OUT} =1V	150			mA
V _{DROP}	Dropout Voltage ¹	I _{OUT} =50mA				
		V _{OUT} =3.3V		335		mV
		V _{OUT} =5.0V		240		mV
$\frac{\Delta V_{out}}{\Delta V_{in} \cdot V_{out}}$	Line Regulation	I _{OUT} =10mA, V _{OUT} +1V≤V _{IN} ≤40V		0.2	0.3	%/V
ΔV _{out}	Load Regulation	V _{IN} =V _{OUT} +1V 1mA≤I _{OUT} ≤50mA		20	40	mV
I _q	Quiescent Current	V _{IN} =V _{OUT} +1V		2.5	5	μA
$\frac{\Delta V_{out}}{\Delta T \cdot V_{out}}$	Output Voltage Temperature Coefficient	I _{OUT} =10mA		±100		ppm/°C
PSRR	Ripple rejection	F=100Hz, Ripple=0.5Vp-p V _{IN} =V _{OUT} +1V		50		dB
T _{SD}	Thermal shutdown temp			140		°C
T _{SH}	Thermal shutdown hysteresis			10		°C

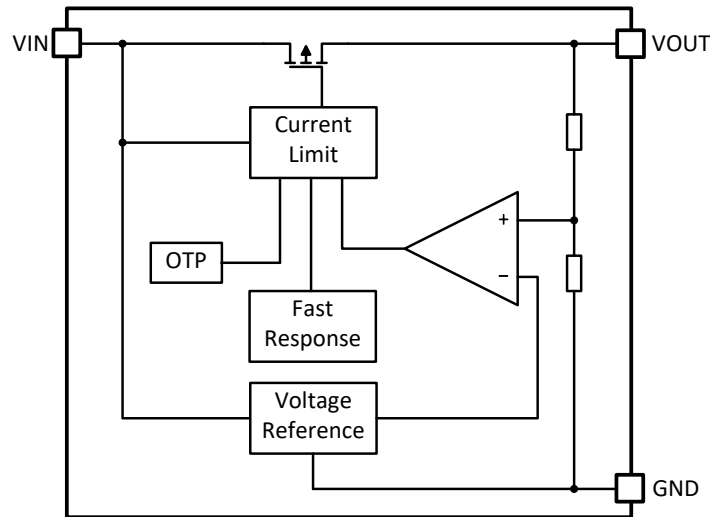
Note:

- 1) V_{DROP}=V_{IN}-V_{OUT} when V_{OUT} drops below 98% of the normal V_{OUT}.

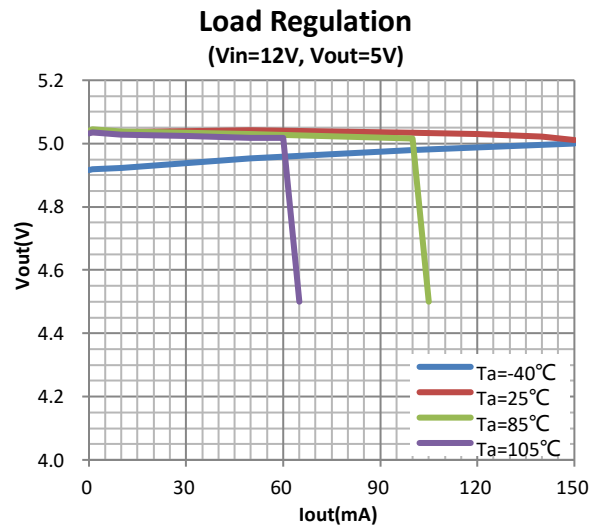
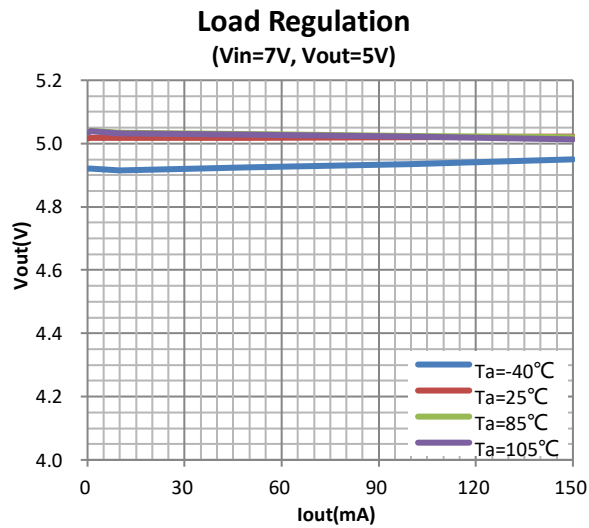
PIN DESCRIPTION

Pin #	Name	Description
1	VIN	Supply voltage input.
2,3,5,6,7	NC	No connection.
4	GND	Ground.
8	VOUT	Output voltage.

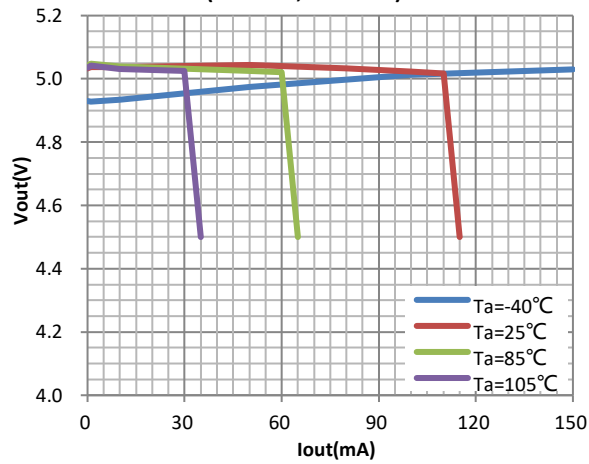
BLOCK DIAGRAM



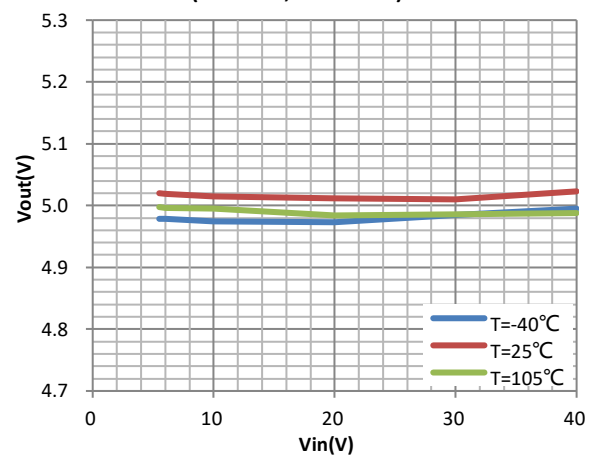
TYPICAL PERFORMANCE CHARACTERISTICS



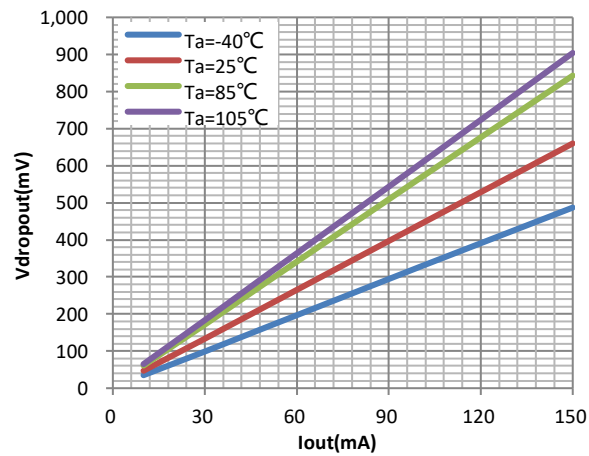
Load Regulation
($V_{in}=16V$, $V_{out}=5V$)



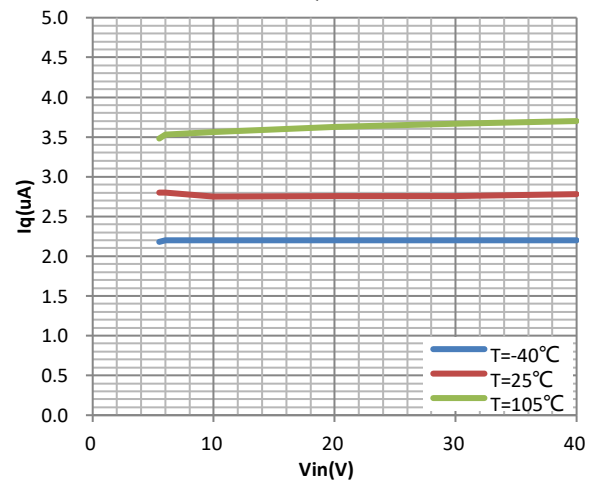
Line Regulation
($V_{out}=5V$, $I_{out}=1mA$)



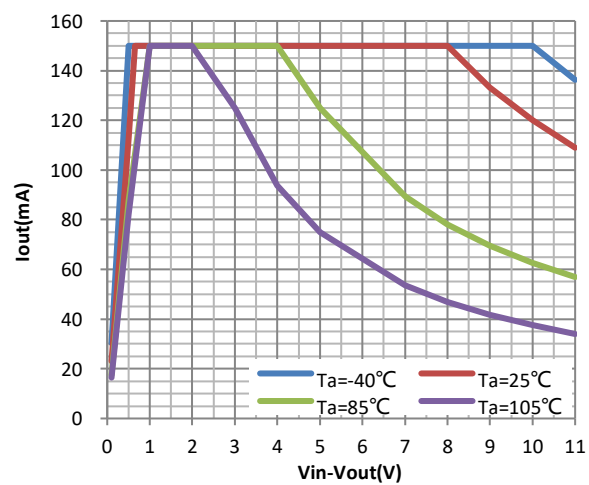
Dropout Voltage
($V_{out}=5V$)



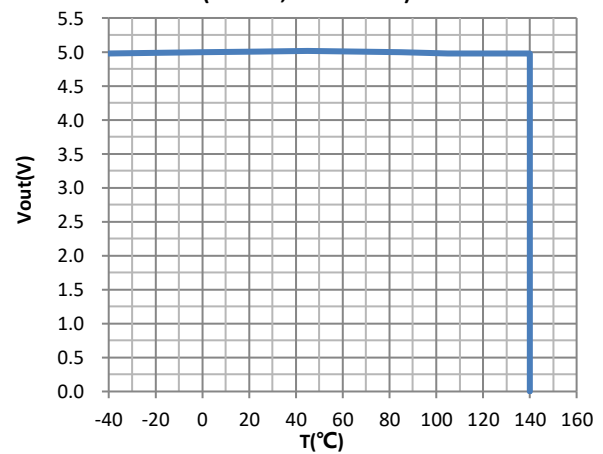
I_Q



Safe Operation Area



Output Voltage vs. Temp.
($V_{in}=6V$, $I_{out}=10mA$)



BL8078Q

EXPLANATION

BL8078Q is a series of low dropout voltage and low power consumption regulator. Its application circuit is very simple, which only needs two outside capacitors. It is composed of these modules: high accuracy voltage reference, current limit circuit, error amplifier, output driver and power transistor.

Current limit module can keep chip and power system away from danger when load current is more than 180mA.

BL8078Q uses trimming technique to assure the accuracy of output value within $\pm 2\%$. At the same time, temperature compensation is elaborately considered in this chip, which makes BL8078Q's temperature coefficient within $\pm 100\text{ppm}/^\circ\text{C}$.

THERMAL CONSIDERATIONS

Knowing the input voltage, the output voltage and the load profile of the application, the total power dissipation can be calculated:

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_Q$$

with

- P_D : continuous power dissipation
- V_{IN} : input voltage
- V_{OUT} : output voltage
- I_{OUT} : output current
- I_Q : quiescent current

The maximum acceptable thermal resistance $R_{\theta JA}$ can then be calculated:

$$R_{\theta JA, \max} = \frac{T_{J, \max} - T_A}{P_D}$$

with

- $T_{J, \max}$: maximum allowed junction temperature
- T_A : ambient temperature

PACKAGE OUTLINE

Package	SOP-8	Devices per reel	2500pcs
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Package dimension:

COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	1.35	1.55	1.75
A1	0.05	—	0.25
A2	1.30	1.40	1.50
A3	0.50	0.60	0.70
b	0.38	—	0.47
b1	0.37	0.40	0.43
c	0.17	—	0.25
c1	0.17	0.20	0.23
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.17	1.27	1.37
L	0.45	0.60	0.80
L1	1.04REF		
L2	0.25BSC		
R	0.07	—	—
R1	0.07	—	—
h	0.30	0.40	0.50
Ø	0°	—	8°
Ø1	15°	17°	19°
Ø2	11°	13°	15°

NOTES:
ALL DIMENSIONS REFER TO JEDEC STANDARD MS-012 AA
DO NOT INCLUDE MOLD FLASH OR PROTRUSION.

Unit: mm